

REMARKS

This Amendment is being filed in response to the Final Office Action mailed June 12, 2008, which has been reviewed and carefully considered.

Claims 1-19 remain in this application, where claims 1 and 13 are independent.

By means of the present amendment, claim 14 has been amended for better form to place it in better form for appeal. Accordingly, no new issues requiring a new search have been introduced and entry of the present amendment is respectfully requested.

In the Final Office Action, claims 1-19 are rejected under 35 U.S.C. §102(e) as allegedly anticipated by U.S. Patent No. 2003/0141807 (Kawase). It is respectfully submitted that claims 1-19 are patentable over Kawase for at least the following reasons.

Kawase is directed to a display, shown in FIG 7, having an electroluminescent layer 8 between electrodes 6, 10 supported on a substrate 4. Overlapping areas of the electrodes 6, 10, shown as a shaded area 12, emit visible light when current flows through the electroluminescent layer 8, thus forming pixels in a display area

22 shown as a dotted rectangle in FIG 7, and described in paragraphs [0076] - [0077].

Kawase strives to reduce resistance by reducing the physical lengths of the arrays of electrodes 6, 10. This is achieved by placing the drive circuits 14, 18 within the segment of pixels, where the drive circuits 14, 18 are connected to the electrodes 6, 10 through conductive tracks 16, 20.

As the drive circuits 14, 18 are within the segment of pixel, and the conductive tracks 16, 20 connect the drive circuits 14, 18 to the electrodes 6, 10, where minimal resistance (and thus minimal length of conductors) is desired, it then follows that the conductive tracks 16, 20 are also close to the drive circuits 14, 18 and thus within display area 22.

FIG 8 of Kawase shows a more conventional display where the drive circuits 14, 18 are outside the display area 22, thus requiring electrodes 6, 10 with longer lengths, as described in paragraph [0079]. That is, Kawase solves the problem of increased resistance and achieves reduced resistance by moving the drive circuits 14, 18 from outside the display area 22 (FIG 8) to within the display area 22 (FIG 7). Further, Kawase is completely silent

about any spurs that extend from any conductor lines.

In stark contrast, the present invention as recited in independent claim 1, and similarly recited in independent claim 13, amongst other patentable elements recites (illustrative emphasis provided):

wherein the display further comprises at least one conductor line extending along an edge of the display over the common substrate and outside the display area, the at least one conductor line comprising at least one layer additional to the plurality of thin film layers defining the array of pixels, and wherein at least one of the row driver circuitry and the column driver circuitry comprises a portion provided on the common substrate outside the display area and which connects to the at least one conductor line through spurs extending from the at least one conductor line, wherein the at least one conductor line extends between the portion and the display area.

A conductor line extending along an edge of the display over the common substrate and outside the display area, and between circuitry and display area, where the circuitry comprises a portion provided on the common substrate outside the display area and which connects to the conductor line through spurs extending from the conductor line, is nowhere disclosed or suggested in Kawase.

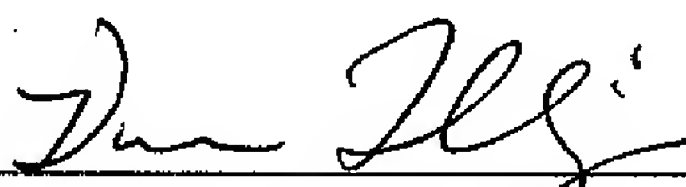
Accordingly, it is respectfully submitted that independent

claims 1 and 13 should be allowable, and allowance thereof is respectfully requested. In addition, it is respectfully submitted that claims 1-12 and 14-19 should also be allowed at least based on their dependence from amended independent claims 1 and 13.

In addition, Applicant denies any statement, position or averment of the Examiner that is not specifically addressed by the foregoing argument and response. Any rejections and/or points of argument not addressed would appear to be moot in view of the presented remarks. However, the Applicant reserves the right to submit further arguments in support of the above stated position, should that become necessary. No arguments are waived and none of the Examiner's statements are conceded.

In view of the above, it is respectfully submitted that the present application is in condition for allowance, and a Notice of Allowance is earnestly solicited.

Respectfully submitted,

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